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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/535,064	05/13/2005	Xavier Robert	S1022.81244US00	1851
46329	7590 10/02/2006		EXAMINER	
STMicroelectronics Inc. c/o WOLF, GREENFIELD & SACKS, PC			VO, HIEN XUAN	
Federal Reserve Plaza			ART UNIT	PAPER NUMBER
600 Atlantic Avenue			2863	
BOSTON, MA 02210-2206			DATE MAILED: 10/02/200	4

**DATE MAILED: 10/02/2006** 

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/535,064	ROBERT, XAVIER				
Office Action Summary	Examiner	Art Unit				
	Hien X. Vo	2863				
The MAILING DATE of this communication app						
Period for Reply	•	•				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tirr vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 13 M	<u>ay 2005</u> .					
3) Since this application is in condition for allowar	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) Claim(s) 1-9 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ ·Claim(s) <u>1,2 and 6</u> is/are rejected.						
7)⊠ Claim(s) <u>3-5 and 7-9</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or	r election requirement.					
Application Papers						
9) The specification is objected to by the Examine	r.					
10)⊠ The drawing(s) filed on <u>13 May 2005</u> is/are: a)⊠ accepted or b)  objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
11) I he oath or declaration is objected to by the Ex	aminer. Note the attached Oπice	Action of form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list	of the certified copies not receive	d.				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08)	Paper No(s)/Mail Da 5)  Notice of Informal P					
Paper No(s)/Mail Date <u>05/13/05</u> . 6) Other:						

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#### **DETAILED ACTION**

#### Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on 05/13/05. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

## Claim Rejections - 35 USC § 112

- The following is a quotation of the second paragraph of 35 U.S.C. 112:
   The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claim 1 rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claim 1 (line 11), the word "means" is preceded by the word(s) "for" in an attempt to use a "means" clause to recite a claim element as a means for performing a specified function. However, since no function is specified by the word(s) preceding "means," it is impossible to determine the equivalents of the element, as required by 35 U.S.C. 112, sixth paragraph. See *Ex parte Klumb*, 159 USPQ 694 (Bd. App. 1967).

### Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claims 1-9 rejected under 35 U.S.C. 102(e) as being anticipated by Regnier (U.S. 2006/0155970).

With respect to claim 1, Regnier discloses monitoring a microprocessor programme by sending time-trackable messages including a monitoring device integrated on the chip of a microprocessor executing a sequence of instructions, comprising (see e.g. abstract): a message calculation means for generating digital messages of different types each corresponding to the execution of an instruction from among a plurality of predetermined instructions, the calculation means being capable of generating several types of messages at the same time (see e.g. claim 1, lines 4-7); a

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buffer memory divided into several blocks (see e.g. Fig.2, item 34), each of which is provided to only store messages of one of the types of messages likely to be generated at the same time (see e.g. paragraph 0031), the size of each block depending on the maximum frequency at which the messages can be stored therein (see e.g. paragraph 0010); a means for, each time one or several messages are simultaneously stored in blocks of the buffer memory, storing in a predetermined block of the buffer memory a coded value designating said blocks of the buffer memory (see e.g. paragraph 0029).

With respect to claim 2, Regnier disclose the invention as claimed including a means for reading in the order of their storage the coded values stored in said predetermined block of the buffer memory, for reading in the order of their storage the messages stored in the block(s) of the buffer memory designated by each coded value, and providing the read messages to an external analysis tool (see e.g. paragraph 0035).

With respect to claim 6, Regnier disclose the invention as claimed including 6. A method for monitoring a microprocessor executing a sequence of instructions, comprising the steps of: a/ generating one or several digital messages respectively corresponding to the execution of one or several instructions from among a plurality of predetermined instructions, several messages that can be generated at the same time (see e.g. paragraph 0013); b/ each storing messages generated at step a/ in a predetermined block of the buffer memory (see e.g. claim 8, lines 6), the messages simultaneously generated at step a/ being stored in separate blocks, and storing at the same time a coded value indicating in which blocks the messages have been stored

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(see e.g. paragraphs 0014 and 0016); c/ recovering the coded value written at step b/, and based on said coded value, recovering the messages stored at step b/ (see e.g. paragraph 0032).

The applied reference has a common assignee with the instant application.

Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

6. Claims 3—5 and 7-9 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hien X. Vo whose telephone number is (571) 272-2282. The examiner can normally be reached on M-F (8:00-5:30) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on (571) 272-2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

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Hien Vo 09/28/06

John Barlow
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